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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
TANAKA, Yoshiyuki; YATABE, Makoto;
SATO, Takeaki; and KAWAMOTO, Kazuya
Serial No:
Filed: August 7, 1998
For: METHOD FOR CONTROLLING NON-
VOLATILE SEMICONDUCTOR MEMORY
SYSTEM

Art Unit:
Examiner:

08/07/98
09/13/98
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Dear Sir:

I hereby certify that

- ☒ a letter of transmittal
☐ check in amount of \$ 1736 as filing fee
☒ patent application (71 pages of specification; 22 claims; 1 pages of abstract
☒ 45 sheet(s) of Formal drawings
☐ executed Declaration and Power of Attorney
☐ executed Assignment, with Recordation Cover Letter and check in amount of \$40
☒ 2 certified copy(ies) of Japanese patent application(s) from which priority is claimed in the subject case pursuant to 35 USC § 119
☐ Preliminary Amendment
☐ Information Disclosure Statement with _ references
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FORM PTO-1082
Case Docket No. 5702D-6844
Date: August 7, 1998
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Dear Sir:

Transmitted herewith for filing is the patent application of
Inventor(s): **Yoshiyuki TANAKA, Makoto YATABE, Takeaki SATO, and Kazuya KAWAMOTO**
For: **METHOD FOR CONTROLLING NON-VOLATILE SEMICONDUCTOR MEMORY SYSTEM**

Enclosed are:

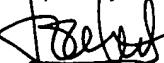
- ☒ 45 Sheets of drawings
- ☐ An assignment of the invention to Kabushiki Kaisha Toshiba. (Will follow)
- ☐ Declaration and Power of Attorney. (Will follow)
- ☒ Certified copy of Japanese Patent Application Nos. 9-214561 filed August 8, 1997 and 10-119099 filed April 28, 1998, from which priority is claimed under 35 USC §119.

| CALCULATION OF FEES | | | | | | | |
|--|--|------------------------------------|---------|----------------------------|--|-----------|---------|
| ITEM | | NO. OF CLAIMS FILED MINUS BASE* | | NO. OF CLAIMS OVER BASE | X SML/G ENTITY FEE | \$ AMOUNT | \$ FEE |
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| **LIST INDEPENDENT CLAIMS 1, 2, 4, 5, 6, 7, 10, 13, 15, 16, 17, 19, 20, and 22 | | | | | | | |

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Respectfully submitted,



Louis A. Mok
Reg. No. 22, 585

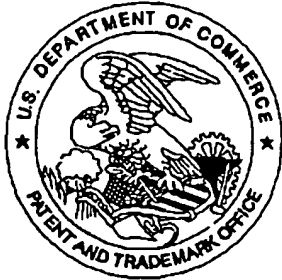
Date: August 7, 1998

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08/07/98

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